L Number	Hits	Search Text	DB	Time stamp
33	1	(20020040459.pn.) and frequency and transfer and clock and parallel	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/27 14:05
34	318	clock with frequency with transfer with rate with signal\$1	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/27 14:06
35	93	(clock with frequency with transfer with rate with signal\$1) and (integrated adj circuit\$1)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/27 15:36
	2	2000298525.pn.	IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/25 15:20
-	1129	714/738.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/25 15:28
_	0	circuit) and semiconductor and test\$3 and (trasmit\$4 with parallel with serial) and (receiv\$3 with serial with parallel) and	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/25 15:33
_	0	(test\$3 with generat\$3) and compar\$5 (714/738.ccls.) and (integrated adj circuit\$1) and semiconductor and test\$3 and (trasmi\$4 with parallel with serial) and (receiv\$3 with serial with parallel)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/25 15:33
-	0	and (test\$3 with generat\$3) and compar\$5 (integrated adj circuit\$1) and semiconductor and test\$3 and (trasmi\$4 with parallel with serial) and (receiv\$3 with serial with parallel) and (test\$3	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/25 15:33
_	2	with generat\$3) and compar\$5 (714/738.ccls.) and (integrated adj circuit\$1) and semiconductor and test\$3 and (transmi\$5 with parallel with serial) and (receiv\$3 with serial with parallel)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/25 16:10
_	118	and (test\$3 with generat\$3) and compar\$5 (integrated adj circuit\$1) and semiconductor and test\$3 and (transmi\$5 with parallel with serial) and (receiv\$3 with serial with parallel) and (test\$3	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/05/25 16:11
-	51	with generat\$3) and compar\$5 (integrated adj circuit\$1) and semiconductor and test\$3 and (transmi\$5 with parallel with serial) and (receiv\$3 with serial with parallel) and (test\$3 with generat\$3) and compar\$5 and (test\$3	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 16:14
-	14	adj mode) (integrated adj circuit\$1) and semiconductor and test\$3 and (transmi\$5 with parallel with serial) and (receiv\$3 with serial with parallel) and (test\$3 with generat\$3) and compar\$5 and (test\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 16:20
-	17	adj mode) and (loopback) (integrated adj circuit\$1) and semiconductor and test\$3 and (transmi\$5 same parallel same serial) and (receiv\$3 same serial same parallel) and (test\$3 with generat\$3) and compar\$5 and (test\$3 adj mode) and (loopback)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:36

151 ((hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) ((hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) (hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) (hold\$3 or stor\$3) adj pulse with circuit\$1) and (generat\$3 with expected with pulse) (hold\$3 or stor\$3) adj pulse with circuit\$1] and (generat\$3 with expected with pulse) (hold\$3 or stor\$3) adj pulse with circuit\$1] and (generat\$3 with expected with pulse) (hold\$3 or stor\$3) adj pulse with circuit\$1] and (generat\$3 with expected with pulse) (hold\$3 or stor\$3) adj pulse) (hold\$3 or stor\$3) (hold\$3					
- 151 ((hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) with pulse) with pulse) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) mad (signal with bits) and (generat\$3 with expected with pulse) and (integrated adj circuit) mad (signal with bits) mad (signal w	-	406	((hold\$3 or stor\$3) with pulse with	USPAT;	2004/05/25
- 151 ((hold33 or stor33) with pulse with circuits1) and (generat53 with expected with pulse) and (integrated adj circuit) BM TOB USPAT; US-POPUB; EPG; JPO; BERWENT; IBM TOB USPAT; US-POPUB; EPG; JPO; DERWENT; IBM TOB USPAT; US-POPUB;			circuit\$1) and (generat\$3 with expected)		17:38
151					
151 (hold33 or stor33) with pulse with circuits1) and (generat33 with expected with pulse) USPAT; USPAT					
		3.53	(/) 7.160	_	2004/05/05
- 48 ((hold3) or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit)	-	151			
BERMENT; IMM TOB USPAT;				1	17:39
1			with puise)	1	
48				i .	
		4.0	//2-1-142		2004/05/25
with pulse) and (integrated adj circuit) EPO; JPO; DERWINT; IBM TDB 2004/05/25 17:40 2004/05/25 2004/05/26 2004/05/26 2004/05/26 2004/05/26 2004/05/26 2004/05/26	-	48		· ·	
- 17 ((hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) - 17 ((hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) - 1 ((hold\$3 or stor\$3) pulse\$1 with circuit\$1) and (generat\$3 adj expected) with pulse \$\frac{1}{2}\$ ((hold\$3 or stor\$3) pulse\$1 with circuit\$1) and ((generat\$3 adj expected) with pulse\$1) and ((integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 ((hold\$3 or stor\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 ((hold\$3 or stor\$3) adj pulse with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 ((hold\$3 or stor\$3) adj pulse with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 ((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 ((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 ((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 ((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (pulse adj sequence) - 0 ((hold\$3 o					17:40
17			with purse, and (integrated adj circuit)		
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17			and (Signal with Dits)	l :	
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and (signal with bits) Comparison of the comp					
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with pulse\$1) and (integrated adj circuit) and (signal with bits) DERWENT; IBM_TDB USPAT; USPGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; U	[· · ·
Circuit) and (signal with bits)					
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- 0 ((hold\$3 or stor\$3) adj pulse with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with circuit\$1) and (pulse\$1 adj sequence) - 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (signal with bits) and (pulse\$1 adj sequence) - 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (signal with bits) and (pulse\$1 adj sequence) - 2 20020040459.pn. - 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 (bold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (pulse adj sequence) - 2 (bold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (generat\$3 with e			and (signal with bits) and (pulse adj	DERWENT;	
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<pre>(integrated adj circuit) and (signal with bits) and (pulse adj sequence) (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with bits) and (pulse\$1 adj sequence) (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (pulse\$1 adj sequence) (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (pulse\$1 adj sequence) (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (integrated adj circuit) usPAT; usPGPUB; l8:01 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (signal with bits) and (pulse\$1 adj sequence) 2 20020040459.pn. ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) ((bold\$3) with pulse adj sequence) ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (pulse adj sequence) ((bold\$3) with pulse adj sequence) ((bold\$4) with pulse adj sequence) ((bold\$4) with pulse adj sequence) ((bold\$4) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (pulse\$1) (pulse Adj pulse\$1) and (pulse adj sequence) (pulse Adj pulse\$1 with circuit\$1) and (pulse adj pulse\$1) (pulse Adj pulse\$1) uspat; (pulse Adj pulse\$1) uspat; (pulse Adj pulse\$1) uspat; (pulse Adj pulse A</pre>	-	0			, ,
bits) and (pulse adj sequence) O (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) O (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with circuit\$1) and (pulse\$1 adj sequence) DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;			circuit\$1) and (generat\$3 adj pulse) and		17:58
- 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) with bits) and (pulse\$1 adj sequence) - 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (signal with bits) and (pulse\$1 adj sequence) - 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1) with circuit\$1) and (integrated adj circuit) uspAT;					
- 0 (((hold\$3 or stor\$3) adj pulse) with circuit\$1) and (generat\$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 0 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) WSPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) - 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (signal with bits) and (pulse\$1) with circuit\$1) and (integrated adj circuit) us-PGPUB; and (signal with bits) and (pulse\$1 adj sequence) - 2 2 20020040459.pn. - 2 ((hold\$3) with pulse with circuit\$1 and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 ((hold\$3) with pulse with circuit\$1 and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 ((hold\$3) with pulse with circuit\$1 and (signal with bits) and (pulse adj sequence) - 2 ((hold\$3) with pulse with circuit\$1 and (signal with bits) and (pulse adj sequence) - 2 ((hold\$3) with pulse with circuit\$1 and (signal with bits) and (pulse adj sequence) - 2 (merat\$3 with expected with pulse) and (signal with bits) and (pulse adj sequence) - 2 (merat\$3 with expected with pulse) and (signal with bits) and (pulse adj sequence) - 2 (merat\$3 with expected with pulse) and (signal with bits) and]		bits) and (pulse adj sequence)		
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(integrated adj circuit) and (signal with bits) and (pulse adj sequence) O (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) US-PGPUB; and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) O (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1 with circuit\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) O ((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1 adj sequence) O ((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1 adj sequence) O ((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1 and (signal with bits) and (pulse\$1 adj sequence) O ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) O (hold\$3) with pulse with circuit\$1) and (signal with bits) O (hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (generat\$3 with expected with	-	0	1		l '
bits) and (pulse adj sequence) O (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) US-PGPUB; and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) O (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (integrated adj circuit) us-PGPUB; and (signal with bits) and (pulse\$1 adj sequence) O ((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1 and (signal with bits) and (pulse\$1 adj sequence) O (hold\$3) with pulse with circuit\$1 and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) O (hold\$3) with pulse with circuit\$1 and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; DERW					11:28
- 0 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) us-pgpub; and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) - 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (signal with bits) and (pulse\$1 adj uspat; us					
- 0 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (generat\$3 adj pulse\$1) USPAT; US-PGPUB; and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) - 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (signal with bits) and (pulse\$1 adj circuit) and (signal with bits) and (pulse\$1 adj sequence) - 2 20020040459.pn. - 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 (mold\$3) with pulse with circuit\$1 and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) - 2 (mold\$3) with pulse with circuit\$1 and (generat\$3 with expected with pulse) and (generat\$3 with expected with pulse) and (pulse adj sequence) - 2 (mold\$3) with pulse with circuit\$1 and (generat\$3 with expected with pulse) and (generat\$4 with pulse) and (generat\$4 with pulse) and (generat\$5 with expected with pulse) and (generat\$6 with pulse) and (generat\$7 with expected with pulse) and (generat\$7 with expected with pulse) and (generat\$8 with expected with pulse) and (generat\$8 with expected with pulse) and (generat\$9 w			 nics) and (baise ad] seducice)		
circuit\$1) and (generat\$3 adj pulse\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) 2 20020040459.pn. 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with (generated adj circuit) and (signal with (integrated adj circuit) and (signal with (pulse adj sequence) US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; DERWENT;			(((holds3 or stors3) add nulses1) with		2004/05/25
and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit) and (signal with bits) and (pulse\$1 adj circuit) and (signal with bits) and (pulse\$1 adj circuit) and (signal with bits) and (pulse\$1 adj circuit) and circuit) and circuit				·	
with bits) and (pulse\$1 adj sequence) 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) 2 20020040459.pn. 2 20020040459.pn. 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with circuit\$1) and (signal with circuit\$1) and (signal with circuit\$1) and (signal with circuit\$1) and (pulse adj sequence) DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; DERWENT; DERWENT;	[11.00
1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) 2 20020040459.pn. 2 20020040459.pn. 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) 3 IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; DERWENT; DERWENT; DERWENT;	[
- 1 (((hold\$3 or stor\$3) adj pulse\$1) with circuit\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) - 2 20020040459.pn. 2 20020040459.pn. 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) 2 ((hold\$3) with pulse adj sequence) 2 (pold\$3) with pulse with circuit\$1) and (signal with bits) and (pulse adj sequence) 2 (pold\$3) with pulse with circuit\$1 and (signal with bits) and (pulse adj sequence) 2 (pold\$3) with pulse with circuit\$1 and (signal with bits) and (pulse adj sequence) 2 (pold\$3) with pulse with circuit\$1 and (signal with bits) and (pulse adj sequence) 2 (pold\$3) with pulse with circuit\$1 and (signal with bits) and (pulse adj sequence) 2 (pold\$4) (pold\$5/25 (pold\$4) (pold\$5/26 (pol			(Paradir and poduction)	'	
circuit\$1) and (integrated adj circuit) and (signal with bits) and (pulse\$1 adj sequence) 2 20020040459.pn. 2 20020040459.pn. 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) 2 (perwent; 18:01 EPO; JPO; DERWENT; 18M TDB USPAT; 2004/05/26 US-PGPUB; EPO; JPO; DERWENT; 18M TDB USPAT; 2004/05/26 US-PGPUB; EPO; JPO; DERWENT; 10:00	_	1	(((hold\$3 or stor\$3) adi pulse\$1) with		2004/05/25
and (signal with bits) and (pulse\$1 adj sequence) - 2 20020040459.pn. 2 20020040459.pn. 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; IBM_TDB USPAT; US-PGPUB; IDS-PGPUB; I					1
sequence) DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; USPAT; USPGPUB; EPO; JPO; Dits) and (pulse adj sequence) DERWENT;					
TBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; USPAT; USPAT; US-PGPUB; USPAT; US-PGPUB; US-PGPUB; US-PGPUB; US-PGPUB; US-PGPUB; US-PGPUB; US-PGPUB; DERWENT; US-PGPUB; DERWENT;					
2 20020040459.pn. 2 20020040459.pn. USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPĀT; USPĀT; US-PGPUB; EPO; JPO; DERWENT; USPĀT; USP			<u> </u>		
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with EPO; JPO; bits) and (pulse adj sequence) DERWENT; DERWENT;	-	2	20020040459.pn.		2004/05/26
- 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with pulse) and (pulse adj sequence) DERWENT; IBM_TDB USPĀT; US-PGPUB; EPO; JPO; DERWENT;			_	US-PGPUB;	09:51
- 2 ((hold\$3) with pulse with circuit\$1) and (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with pulse) and (pulse adj sequence) IBM_TDB USPĀT; US-PGPUB; EPO; JPO; DERWENT;				EPO; JPO;	
- 2 ((hold\$3) with pulse with circuit\$1) and USPAT; 2004/05/26 (generat\$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) DERWENT;				DERWENT;	
(generat\$3 with expected with pulse) and US-PGPUB; 10:00 (integrated adj circuit) and (signal with EPO; JPO; bits) and (pulse adj sequence) DERWENT;				IBM_TDB	
(integrated adj circuit) and (signal with EPO; JPO; bits) and (pulse adj sequence) DERWENT;	-	2	((hold\$3) with pulse with circuit\$1) and	· ·	
bits) and (pulse adj sequence) DERWENT;			(generat\$3 with expected with pulse) and	I	10:00
· IBM TDB			bits) and (pulse adj sequence)		
	<u> </u>			IBM_TDB	

	11	((hold\$3) with pulse with circuit\$1) and (integrated adj circuit) and (signal with	USPAT; US-PGPUB;	2004/05/26 10:04
		bits) and (pulse adj sequence)	EPO; JPO;	10.01
			DERWENT; IBM TDB	
_	9	((hold\$3) with pulse with circuit\$1) and	USPAT;	2004/05/26
		(generat\$3 with pulse\$1) and (integrated	US-PGPUB;	10:11
		adj circuit) and (signal with bits) and (pulse adj sequence)	EPO; JPO; DERWENT;	
			IBM_TDB	
-	0	((hold\$3) with pulse with circuit\$1) and	USPAT;	2004/05/26 10:12
		(generat\$3 with pulse\$1) and (integrated adj circuit) and (signal with bits) and	US-PGPUB; EPO; JPO;	10:12
		(pulse adj sequence) and flipflop	DERWENT;	
_	2	((hold\$3) with pulse with circuit\$1) and	<pre>IBM_TDB USPAT;</pre>	2004/05/26
	4	(generat\$3 with pulse\$1) and (integrated	US-PGPUB;	10:13
		adj circuit) and (signal with bits) and	EPO; JPO;	
		(pulse adj sequence) and(flip adj flop)	DERWENT; IBM TDB	
-	2	((hold\$3) with pulse with circuit\$1) and	USPAT;	2004/05/26
	:	(generat\$3 with pulse\$1) and (integrated adj circuit) and (signal with bits) and	US-PGPUB; EPO; JPO;	10:14
		(pulse adj sequence) and (flip adj flop)	DERWENT;	
	2		IBM_TDB	2004/05/26
-	3	((hold\$3) with pulse with circuit\$1) and (integrated adj circuit) and (signal with	USPAT; US-PGPUB;	2004/05/26 10:48
		bits) and (pulse adj sequence) and (flip	EPO; JPO;	
		adj flop)	DERWENT; IBM TDB	
-	85	((hold\$3) with pulse with circuit\$1) and	USPAT;	2004/05/26
		(integrated adj circuit) and (signal with	US-PGPUB;	10:49
		bits) and ((flip adj flop) with circuits)	EPO; JPO; DERWENT;	
			IBM_TDB	
-	8	((hold\$3) with pulse with circuit\$1) and (integrated adj circuit) and (signal with	USPAT; US-PGPUB;	2004/05/26
1		bits) and ((flip adj flop) with circuits)	EPO; JPO;	10.35
		and (((EX adj "OR") or (exclusive adj	DERWENT;	
_	26	<pre>"or")) with circuits) ((hold\$3) with pulse with circuit\$1) and</pre>	IBM_TDB USPAT;	2004/05/26
		(integrated adj circuit) and (compar\$5	US-PGPUB;	11:43
		with circuit\$1) and (generat\$3 with expected) and (compar\$5 with pulse\$1)	EPO; JPO; DERWENT;	
			IBM_TDB	
_	2	((hold\$3) with pulse with circuit\$1) and	USPAT;	2004/05/26
		(integrated adj circuit) and (compar\$5 with circuit\$1) and (generat\$3 adj	US-PGPUB; EPO; JPO;	11:03
		expected) and (compar\$5 with pulse\$1)	DERWENT;	
_	1	(5787114.pn.) and mode	IBM_TDB USPAT;	2004/05/26
		(1.1.1.1.1.)	US-PGPUB;	12:26
			EPO; JPO;	
			DERWENT; IBM TDB	
_	2	5043931.pn.	USPĀT;	2004/05/26
			US-PGPUB; EPO; JPO;	12:29
			DERWENT;	
_	. 2	5111451 pp	IBM_TDB	2004/05/26
.	2	5111451.pn.	USPAT; US-PGPUB;	2004/05/26 12:32
			EPO; JPO;	
			DERWENT; IBM TDB	
_	0	53434611.pn.	USPĀT;	2004/05/26
			US-PGPUB; EPO; JPO;	12:32
			DERWENT;	
1			IBM TDB	

	2	5343611.pn.	USPAT;	2004/05/26
	_	• "	US-PGPUB;	12:32
			EPO; JPO;	
			DERWENT;	
	1		IBM TDB	
_	0	(BARTON.IN) and (loop adj back)	USPAT;	2004/05/26
			US-PGPUB;	12:34
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	0	BARTON.IN	USPAT;	2004/05/26
			US-PGPUB;	12:34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	4550	BARTON.IN.	USPAT;	2004/05/26
			US-PGPUB;	12:34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	7	(BARTON.IN.) and (loop adj back)	USPAT;	2004/05/26
			US-PGPUB;	12:46
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	6	(BARTON.IN.) and (loop adj back) and	USPAT;	2004/05/26
		switch\$3	US-PGPUB;	12:58
		·	EPO; JPO;	1
			DERWENT;	
			IBM_TDB	
_	260	(switch\$3 with input with (test adj	USPAT;	2004/05/26
		signal)) and (integrated adj circuit)	US-PGPUB;	17:17
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	27	(compar\$5 with circuit\$1) same ("AND"	USPAT;	2004/05/27
		with circuit\$1) same (NOR with circuit\$1)	US-PGPUB;	13:59
		same (flip adj flop) same latch	EPO; JPO;	
			DERWENT;]
			IBM TDB	